TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (L^2 - π -MOSV)

2SK2614

Chopper Regulator, DC-DC Converter and Motor Drive Applications

• 4-V gate drive

• Low drain-source ON-resistance : $R_{DS (ON)} = 0.032 \Omega (typ.)$

• High forward transfer admittance : $|Y_{fs}| = 13 \text{ S (typ.)}$

• Low leakage current : I_{DSS} = 100 μA (max) (V_{DS} = 50 V)

• Enhancement mode : $V_{th} = 0.8$ to 2.0 V ($V_{DS} = 10$ V, $I_D = 1$ mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	50	V
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	50	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	ΙD	20	Α
	Pulse (Note 1)	I_{DP}	50	Α
Drain power dissipation (Tc = 25°C)		P_{D}	40	W
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C

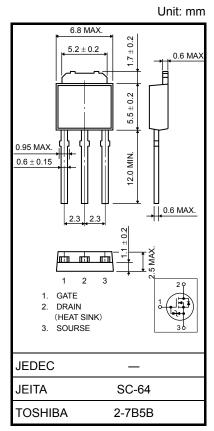
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

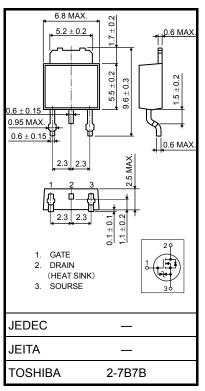
Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	125	°C/W

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.36 g (typ.)



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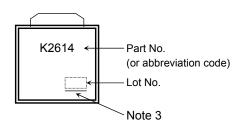
Electrical Characteristics (Ta = 25°C)

Charac	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cutoff curr	ent	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source bre	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	50	_	_	V
Gate threshold v	oltage/	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON-resistance		D	V _{GS} = 4 V, I _D = 5 A	_	0.055	0.08	Ω
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 10 A	_	0.032	0.046	
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 10 A	7	13	_	S
Input capacitano	e	C _{iss}		_	900	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	130	_	pF
Output capacitance		C _{oss}		_	370	_	
Switching time	Rise time	t _r	V _{GS} 0 V	_	15	_	ns
	Turn-on time	t _{on}		_	25	_	
	Fall time	t _f		_	30	_	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w = 10 μs	_	100	_	
Total gate charge (gate-source plus gate-drain) Gate-source charge Gate-drain ("Miller") charge		Qg	$V_{DD} \approx 40 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 20 \text{ A}$	_	25	_	
		Q _{gs}			19		nC
		Q _{gd}			6		

Source-Drain Ratings and Characteristics (Ta = 25°C)

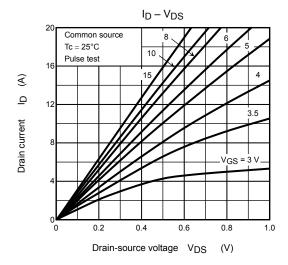
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	20	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	50	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 20 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 20 A, V _{GS} = 0 V, dI _{DR} / dt = 50 A / μs	1	60		ns
Reverse recovery charge	Q _{rr}	1DR - 20 Λ, VGS - 0 V, UDR / UL - 30 A / μs	_	45	_	μC

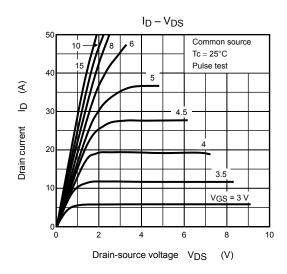
Marking

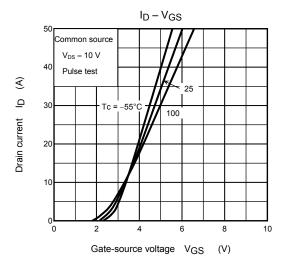


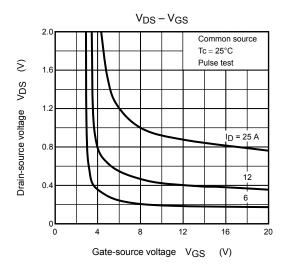
Note 3 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

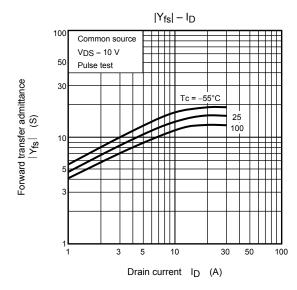
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

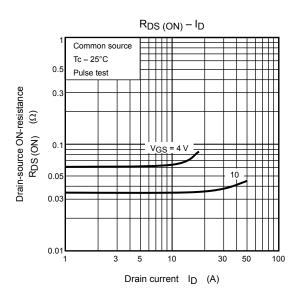




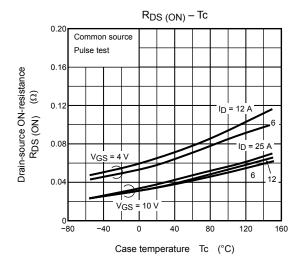


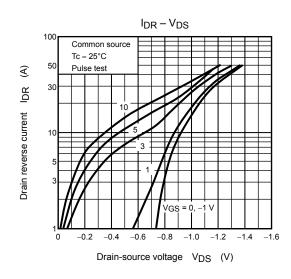


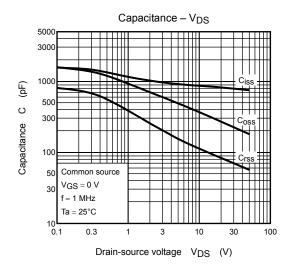


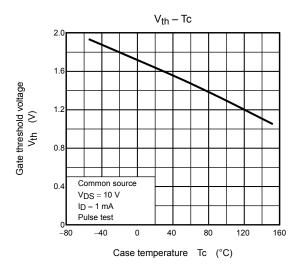


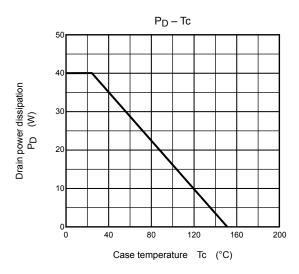
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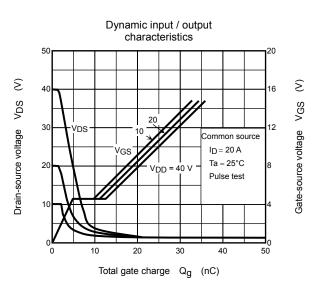




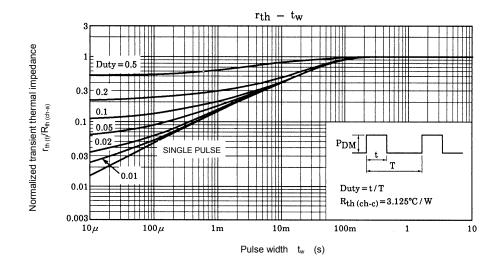


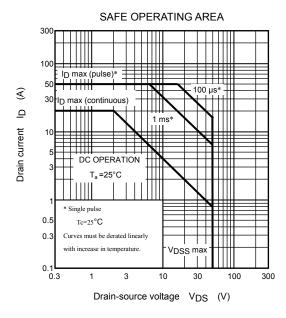






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